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L&L-I0232

CERTIFICATION OF MAILING

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By: Markus WolfDate: January 29, 2004IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/047,013 Confirmation No. 5714
Applicant : Wolfgang Rösner et al.
Filed : January 16, 2002
Title : Method for Producing a Vertical Semiconductor Transistor Component and Vertical Semiconductor Transistor Component
Examiner : Fazli Erdem
Docket No. : L&L-I0232
Group Art Unit : 2826
Customer No. : 24131

A M E N D M E N T

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the non-final Office action dated November 10, 2003, kindly amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.